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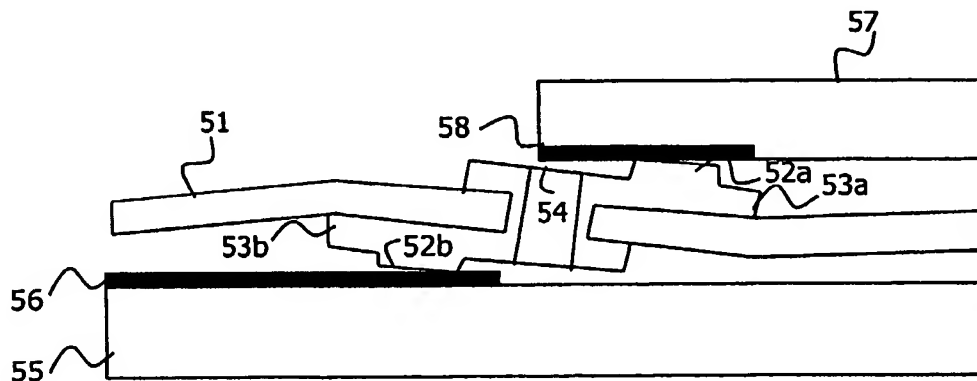
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(54) Title: INTERFACE COMPRISING A THIN PCB WITH PROTRUSIONS FOR TESTING AN INTEGRATED CIRCUIT



(57) Abstract: The invention relates to a test device for testing an integrated circuit called circuit to be tested, comprising one of a plurality of housings in a printed circuit called main circuit. The device comprises an insulating membrane of soft material having two opposite surfaces covered by two conductive layers interconnected by via holes and intended to be in contact with the circuit to be tested and the main circuit respectively, under the influence of a pressure exerted during the test. Protrusions are arranged on at least one of the two layers in a predefined pattern as a function of pins, tabs, pads etc. of the circuit to be tested so as to ensure a contact quality between said layer and the circuit (to be tested or the main circuit) having contact with said layer under the influence of said pressing action.

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